

ABSTRACT

Multiple instruction pipes may share access to a common resource. The instruction pipes may be able to process data from the resource at a faster rate than the rate at which the resource may furnish data to the instruction pipe. An instruction pipe therefore, when it reads a new instruction from an instruction pipestage, determines whether valid data associated with the new instruction can be written to a next instruction pipestage. The instruction pipe stalls processing of the new instruction until valid data associated with the new instruction can be written to the next instruction pipestage.

CONFIDENTIAL